

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re application of:** Rajski et al.

**Application No.** To be assigned

**Filed:** February 10, 2004

**Confirmation No.** To be assigned

**For:** ARITHMETIC BUILT-IN SELF TEST OF  
MULTIPLE SCAN-BASED INTEGRATED  
CIRCUITS

**Examiner:** Unknown

**Art Unit:** To be assigned

**Attorney Reference No.** 1011-67625

**CERTIFICATE OF MAILING**

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450 on the date shown below.

Attorney  
for Applicant(s)

Date Mailed February 10, 2004

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**PRELIMINARY AMENDMENT**

Please amend the referenced application as follows:

**Amendments to the Specification** begin on page 2.

**Amendments to the Claims** are reflected in the listing of claims, which begins on page 6.

**Remarks** begin on page 10.

